

Fig. 10

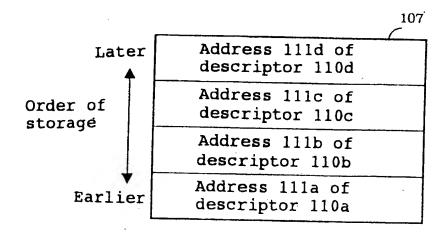


Fig. 11 PRIOR ART Address 111a of descriptor 110a Order of Stores at the same time when storage 107 descriptor 110a is created Later Address 111d of descriptor 110d Address 111c of descriptor 110c Address 111b of Earldescriptor 110b ier Upon receiving end-of-send instructions $\stackrel{\downarrow}{+}$ 113, fetches and discards Address 111a of descriptor 110a

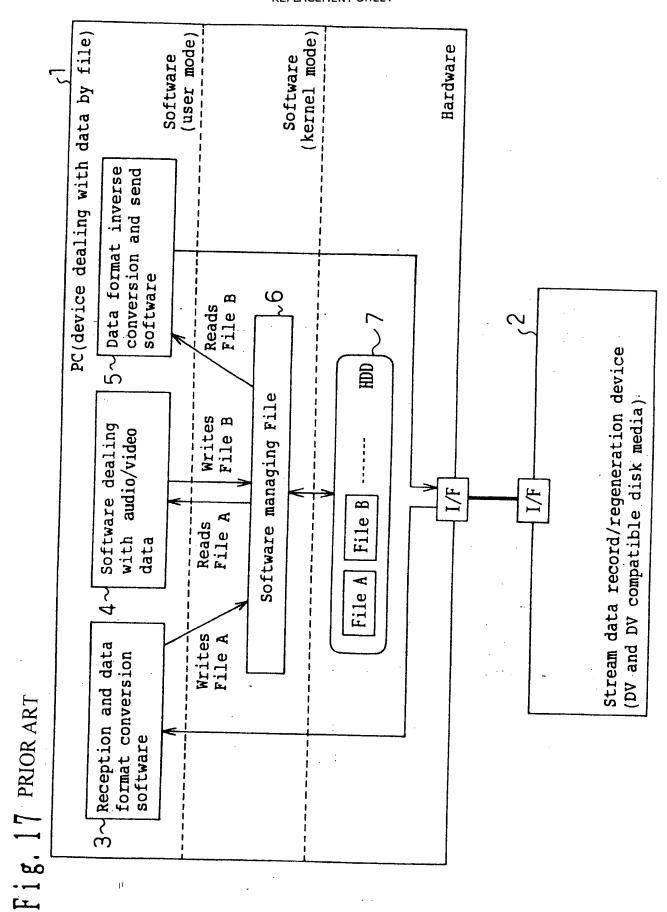


Fig. 24 PRIOR ART

602	channel	63	63	63	63
OPCR[0] of transmitter 602	p2p	0	0	1	-
OPCR tran	pcc	0	-	0	1
f 601	channel number	63	63	63	63
iPCR[0] of receiver 601	р2р	0	0	1	1
iP(pcc	0	1	0	1/0
		Initial condition	FIG. 6	FIG. 7	FIG. 8

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Fig. 28

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Comments		PC 701 allocates			DV 702 releases	resources
channel number	63	0	0	63	63	
p2p	0	_	-	0	0	
pcc	0	0	_	-	0	
	Initial condition	Start-of-reception of PC 701	Start-of-regeneration of DV 702	Stop-of-reception of PC 701	Stop-of-regeneration of DV 702	

Fig. 29

					
Comments		DV 702 allocates			DV 702 allocates resources
channel number	63	63	63	63	63
р2р	0	0	_	0	0
pcc	0	,		-	0
	Initial condition	Start-of-regeneration of DV 702	Start-of-reception of PC 701	Stop-of-reception of PC 701	Stop-of-regeneration of DV 702

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Comments		DV 702 allocates resources			
channel	63	63	63	63	63
ргр	0	0	_	_	0
pcc	0		_	0	0
	Initial condition	Start-of-regeneration of DV 702	Start-of-reception of PC 701	Stop-of-regeneration of DV 702	Stop-of-reception of Pc 701